

Fig. 1 (PRIOR ART)

App No.: Not Yet Assigned
Inventor: Chandra Mouli, et al.

Docket No.: M4065.0761/P761

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE
TRANSFER AND REDUCED LAG AND METHOD OF

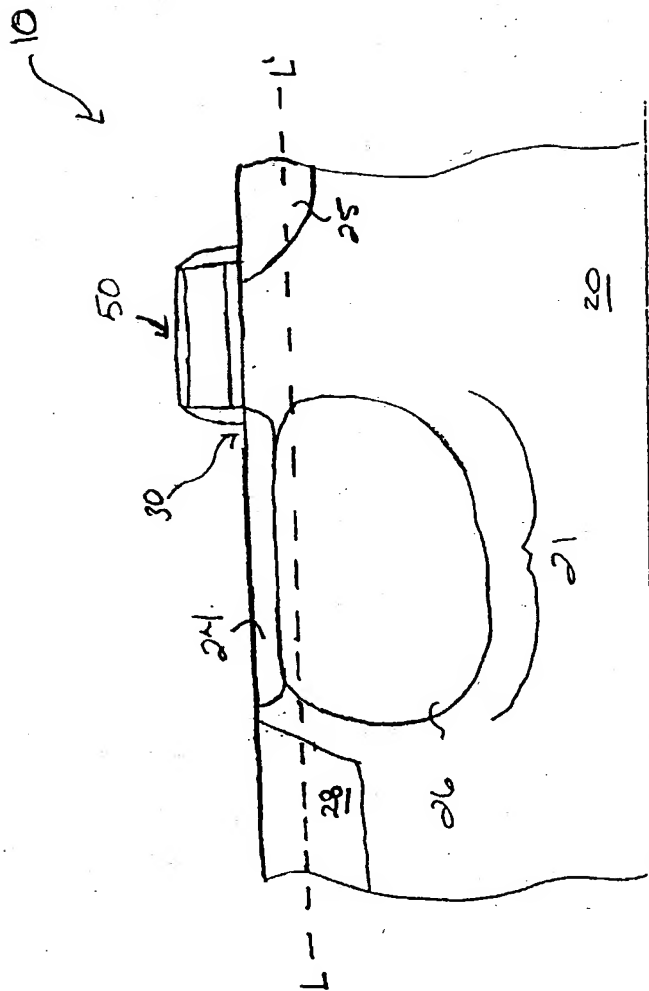


Fig. 2 (Prior Art)

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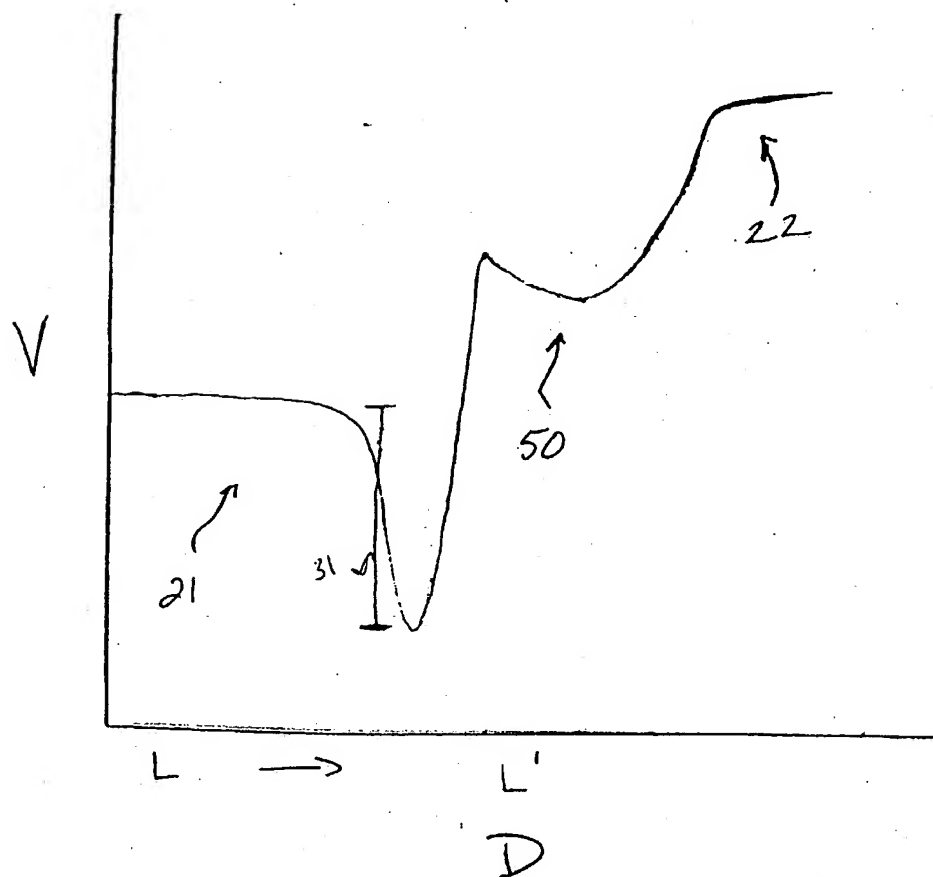


Fig. 3

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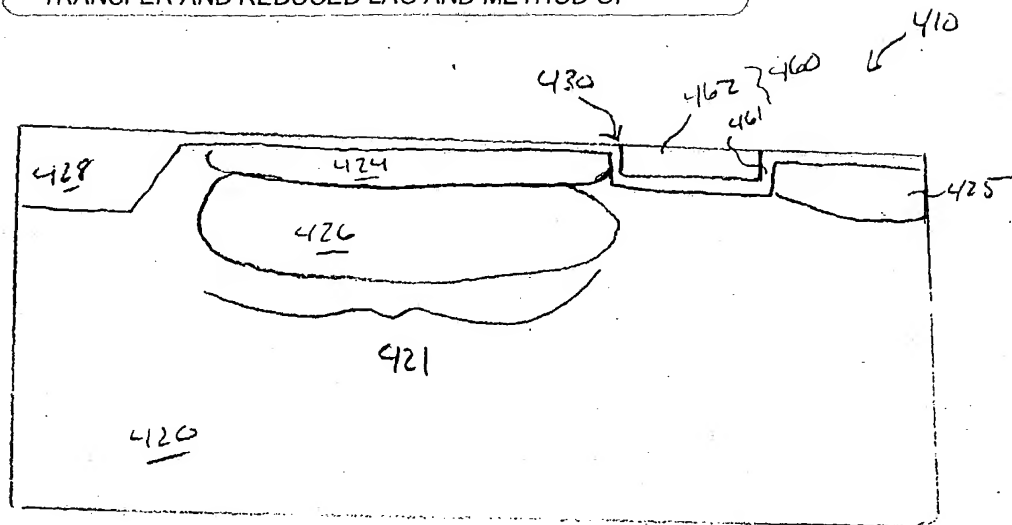


FIG. 4

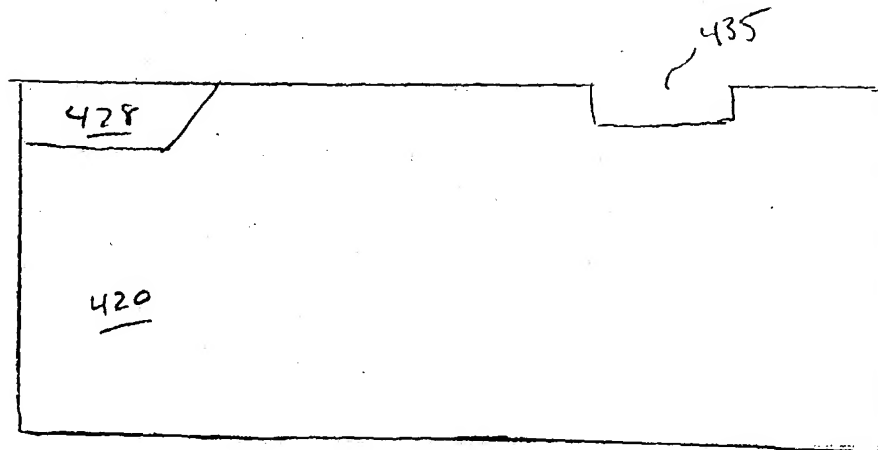


FIG 5A

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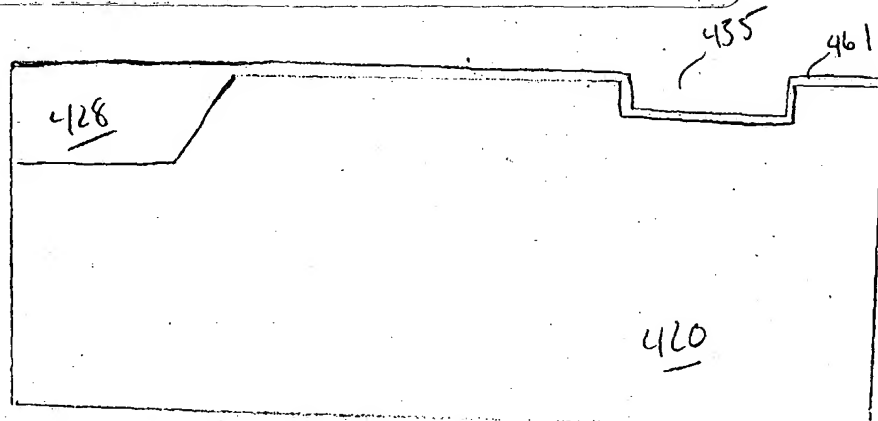


FIG. 5B

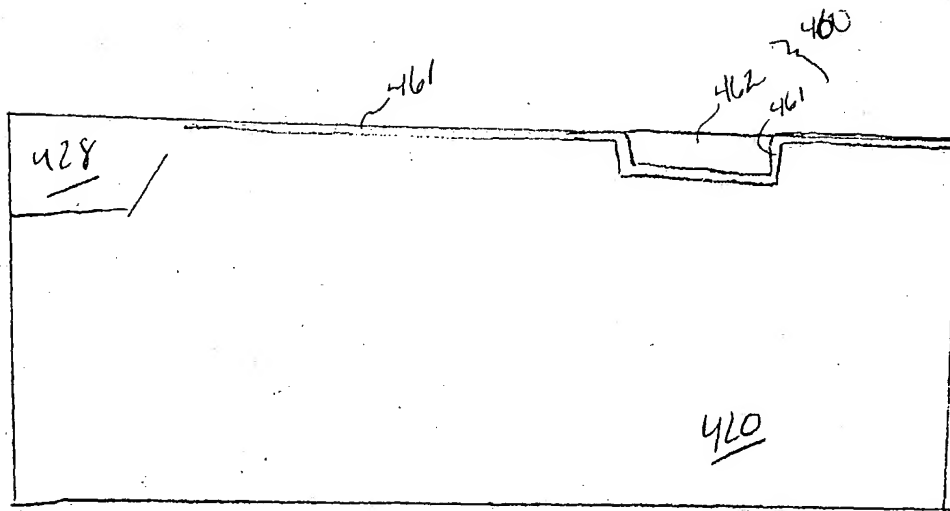


FIG. 5C

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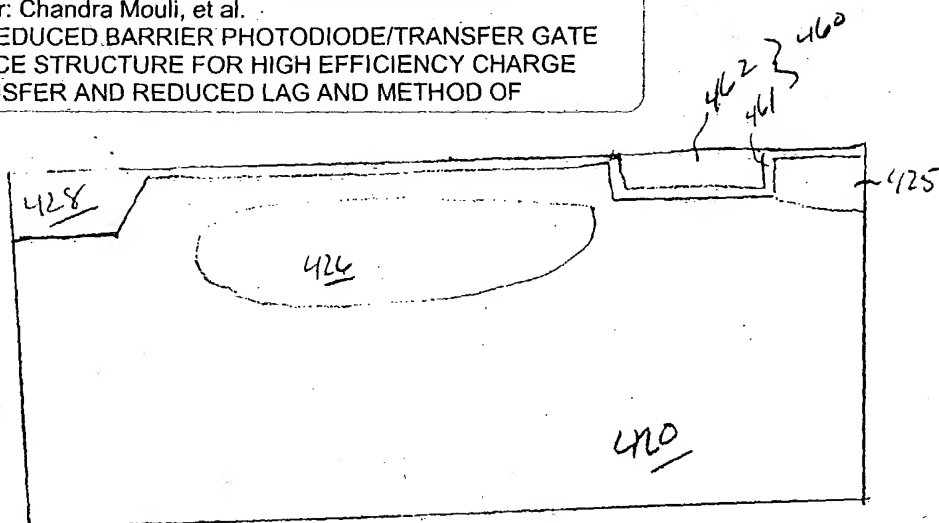


FIG. 5D

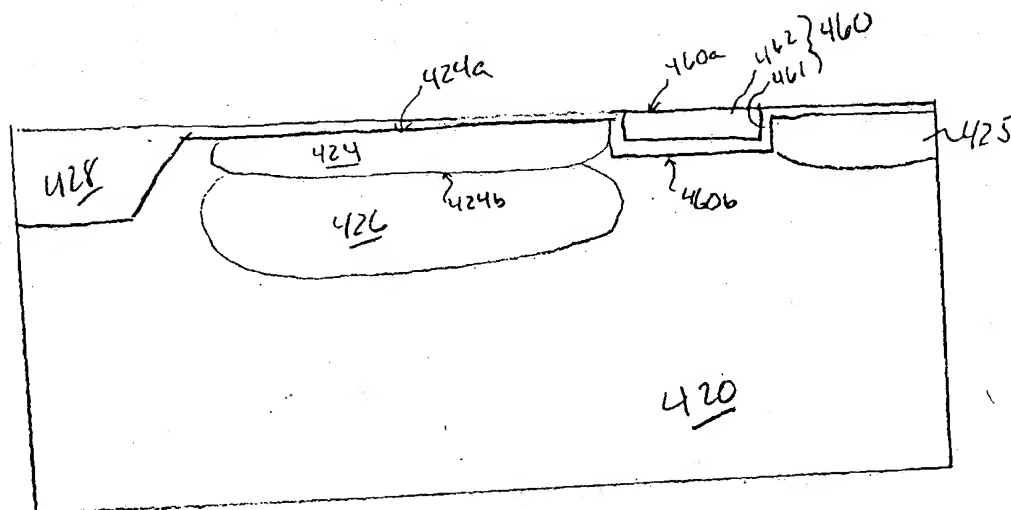


FIG. 5E

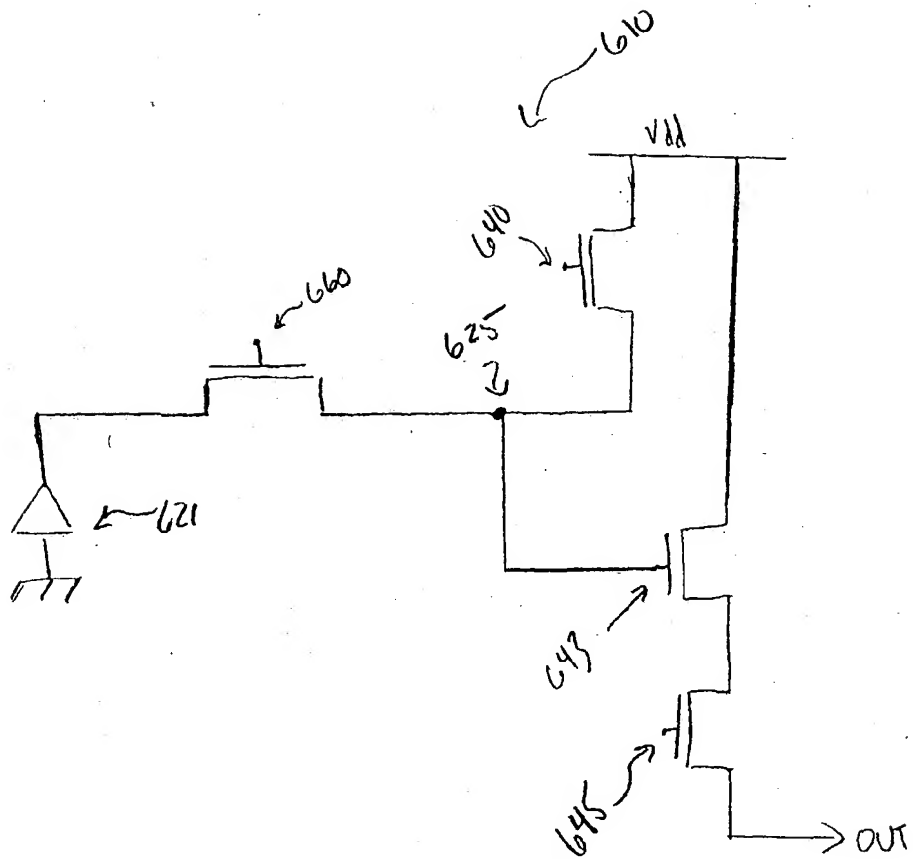


FIG. 6

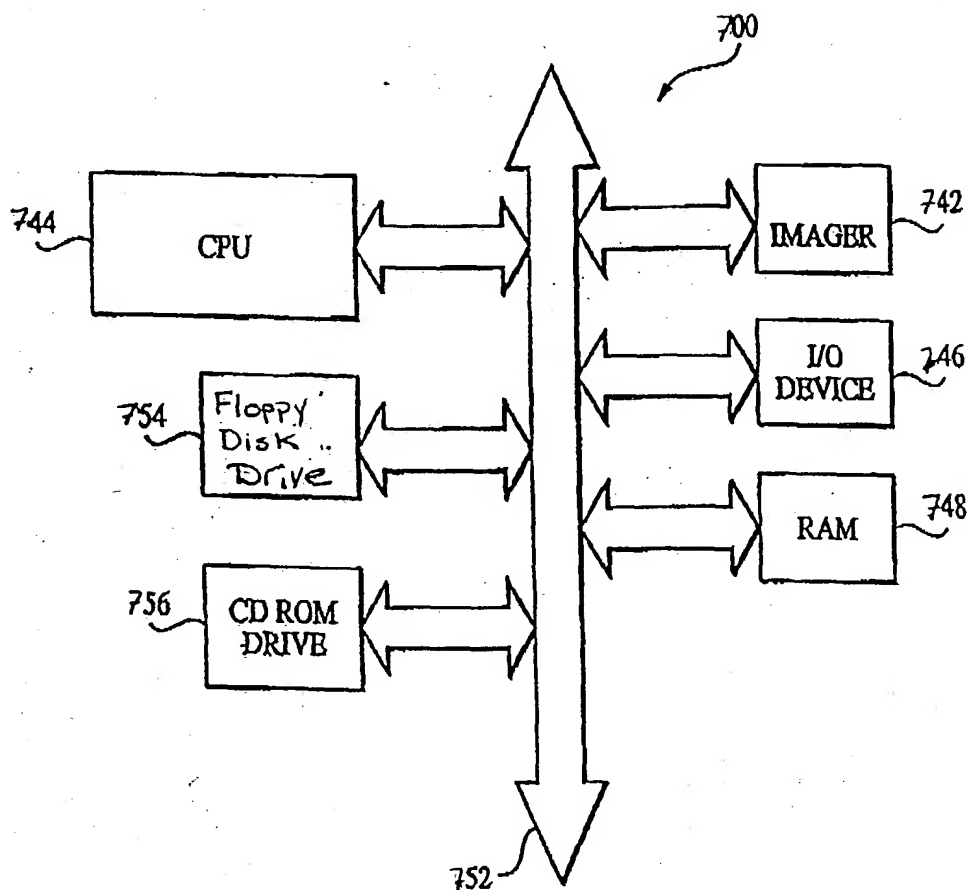


Fig. 7.